CONTENTS:

PAGE1 - CONTENTS
PAGE2 - CAMERA

REVISION HISTORY:
R0 - 20/04/2022 - Initial prototype
R1 - 26/09/2022 - PVT - removed SOT23 crypto chip
R1 = R2\left(\frac{V_{out}}{0.6} - 1\right)

V_{out} = 0.6\left(\frac{R1}{R2} + 1\right)

ATSHA204A I2C address: C8 unless reconfigured

AP3445 and AP3429K both work in this location

R1 = R2\left(\frac{V_{out}}{0.6} - 1\right)

V_{out} = 0.6\left(\frac{R1}{R2} + 1\right)